1. GENERAL INFORMATION

<table>
<thead>
<tr>
<th>Award</th>
<th>Programme Title</th>
<th>Duration</th>
<th>Mode of study</th>
</tr>
</thead>
<tbody>
<tr>
<td>MSc</td>
<td>Low Power System Integration (CEESI)</td>
<td>3-4 years</td>
<td>Modular/part-time/distance learning</td>
</tr>
<tr>
<td>PG Diploma</td>
<td>Low Power System Integration (CEESI)</td>
<td>2-3 years</td>
<td>Modular/part-time/distance learning (exit award only)</td>
</tr>
<tr>
<td>PG Certificate</td>
<td>Low Power System Integration (CEESI)</td>
<td>2 years</td>
<td>Modular/part-time/distance learning</td>
</tr>
</tbody>
</table>

School | Computer Science
Faculty | Engineering & Physical Sciences
Awarding Institution | The University of Manchester
Programme Accreditation | None
Relevant QAA benchmark(s) | N/a – currently only at undergraduate level

2. AIMS OF THE PROGRAMME(S) (must include separate aims for PG Certificate and PG Diploma awards)

The programme aims to: **(NB PG Diploma is exit award only)**:

01. At PG Diploma level: Produce the highest quality of computing professionals in the area of low power systems integration

02. Provide a vehicle for dissemination of leading-edge knowledge and skills, focusing on the research strengths of a large School in this area of knowledge

03. Continue to attract the highest-quality students from the UK and overseas, offering the opportunity to focus on one specialisation

04. Provide an opportunity to engage in a small research project in this area

05. At MSc level: As above except that in place of 04:

   Offer the opportunity to focus on one of a range of specialisations.

06. At MSc level: Provide high quality training and experience in research in Low Power System Integration

3. INTENDED LEARNING OUTCOMES OF THE PROGRAMME(S)

**NB These Learning Outcomes only apply to the course units and project which are under the direct supervision of academic staff in the Department of Computer Science at Manchester**

(must include separate outcomes for PG Certificate and PG Diploma awards)

A. Knowledge & Understanding

Students will be able to:
A1. *(All levels)* Acquire a knowledge of a range of advanced topics in Low Power System Integration beyond undergraduate level and at the forefront of research

A2. *(All levels)* Understand the principles of the ARM and Thumb instruction sets and their practical use

A3. *(MSc & PG Diploma)* Have a knowledge & understanding of research methodology & practice

A4. *(All levels)* Understand the principles of low-power RISC processor design

A5. *(All levels)* Understand the design of memory hierarchies for power-efficient systems, and acquire an ability to apply a systematic methodology to memory hierarchy design

A6. *(All levels)* Understand the system-level issues involved in designing a particular power-sensitive application

A7. *(All levels)* Understand the potential advantages of asynchronous systems

A8. *(All levels)* Understand asynchronous data and control protocols

### Learning & Teaching Processes (to allow students to achieve intended learning outcomes)

**At MSc, PG Diploma & PG Certificate levels**

Each advanced course unit utilises methods appropriate to the subject matter.

**At MSc, PG Diploma & PG Certificate levels**

Small group lectures, supervised laboratory work, mini-projects (group & individual) and independent preparatory learning are the main vehicles for dissemination of knowledge & understanding during the first half of the programme

Following the taught part of the programme, students undertake a programme of supervised individual research, leading to a 90 cr dissertation at MSc level and a 30 cr dissertation at PG Diploma level

### Assessment (of intended learning outcomes)

Course units are assessed by a mixture of written examinations, computer-based practical work, and a range of coursework assessments including assessed miniprojects, group projects, reports, essays etc.

The research project includes an oral presentation of the research, and examination of the dissertation by two internal examiners and an external examiner

### B. Intellectual Skills

Students will be able to:

B1. Develop original ideas in a research context *(MSc and PG Diploma levels only)*

B2. Use methodologies for development of integrated systems at an advanced level *(All)*

B3. Perform problem-solving in academic and industrial environments *(All)*

### Learning & Teaching Processes

**B1.** is mainly demonstrated during the research project.

The intellectual ability B2. is learned through online tutorials, bulletin board discussion groups and

### Assessment

B1. & B3 are developed and assessed during the research project through presentation of a seminar and examination of the dissertation.

B2. is assessed through laboratory exercises, either marked on-line or by written report.
C. Practical Skills

Students will at MSc, PG Diploma & PG Certificate levels be able to:

C1. Develop applications to satisfy given requirements
C2. Organise & pursue a scientific or industrial research project (MSc and PG Diploma only)
C3. Use, manipulate and develop large computational systems
C4. Perform independent information acquisition and management

Learning & Teaching Processes

C1. and C3. are demonstrated in practical lab exercises and mini-projects, as well as during the research project.
C2. and C4. are demonstrated during the research project.
The practical skill C4. is demonstrated in the preliminary preparation for each course unit

Assessment

C1. and C3. are assessed through laboratory exercises, either marked on-line or by written report.
C2. and C4. are developed and assessed during the research project through presentation of a seminar and examination of the dissertation
C4. is also assessed by a report or marked presentation in some course units.

D. Transferable Skills and Personal Qualities

Students will be able to:

D1. Work effectively as a team member (MSc, PG Diploma & PG Certificate)
D2. Prepare and present seminars to a professional standard (MSc level only)
D3. Write theses and reports to a professional standard (MSc and PG Diploma)
D4. Perform independent and efficient time-management (MSc, PG Diploma & PG Certificate)

Learning & Teaching Processes

D1. is evident in team practical project used in a number of course units
D2. is demonstrated during the research project seminar and also within a number of course units.

Assessment

D1. is assessed through reports and marked presentations.
D2. is assessed by two internal examiners during the research project seminar, who provide feedback on presentation skills.
### 4. THE STRUCTURE OF THE PROGRAMME(S)

#### Programme structure and credits

<table>
<thead>
<tr>
<th>Year</th>
<th>Course Unit</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>Year 1</td>
<td>One of CS70042 or Optional course unit in Semester 1</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>One of COMP70212 or Optional course unit in Semester 2</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>Progression to Year 2 is based on only taking a maximum of 2 course units under the 40% pass level over all units studied. The majority of course units are taught distance learning, employing different established DL methods</td>
<td></td>
</tr>
<tr>
<td>Year 2</td>
<td>One of COMP7042 or Optional course unit in Semester 1</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>One of COMP70212 or Optional course unit in Semester 2</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>Progression to Year 2 is based on only taking a maximum of 2 course units under the 40% pass level over all units studied. Most course units are assessed through coursework (66%) and end-of-unit examination (34%)</td>
<td></td>
</tr>
<tr>
<td>Year 3</td>
<td>One of COMP7042 or Optional course unit in Semester 1</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>One of COMP70212 or Optional course unit in Semester 2</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>Progression to project if 90 credits achieved</td>
<td></td>
</tr>
<tr>
<td>Year 4</td>
<td>Research project – selected with academic staff in the research area, either from topics suggested by the research group or topics selected from the workplace</td>
<td>90 (MSc) 30 (PG Dip exit with 90 + 30)</td>
</tr>
</tbody>
</table>
5. STUDENT INDUCTION, SUPPORT AND DEVELOPMENT (in order to deliver the intended learning outcomes, including dissertation support and guidance)

<table>
<thead>
<tr>
<th>Induction</th>
</tr>
</thead>
<tbody>
<tr>
<td>Year 1/Year 2/Year 3</td>
</tr>
<tr>
<td>Course units are taught in distance learning mode.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Year 4</th>
</tr>
</thead>
<tbody>
<tr>
<td>To continue towards the research project for MSc award, students need to pass the taught component. For PG Certificate exit award, students need to pass 60 credits of taught course units. For PG Diploma, students need to pass the taught component to progress to the research project. MSc and Diploma students select their research project from a wide range of proposed projects, and also by individual agreement with supervisors.</td>
</tr>
<tr>
<td>Students have access to the Programme Director throughout the programme. They are encouraged to contact the Director when problems arise and are informed of this during the introductory period. The Department also has a drop-in Advice Centre for lunch-time help-sessions. During the period of the research project, an individual assigned supervisor is also available. Relationship with the supervisor is outlined in the Programme Handbook and the Research Skills course unit.</td>
</tr>
</tbody>
</table>
### 6. CURRICULUM MAP OF COURSE UNITS AGAINST INTENDED LEARNING OUTCOMES OF THE PROGRAMME

<table>
<thead>
<tr>
<th>Course Unit Title and Code (including dissertations and other programme components)</th>
<th>Knowledge &amp; Understanding (A)</th>
<th>Intellectual Skills (B)</th>
<th>Practical Skills (C)</th>
<th>Transferable Skills &amp; Personal Qualities (D)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Code</td>
<td>Course Unit title</td>
<td>A1</td>
<td>A2</td>
<td>A3</td>
</tr>
<tr>
<td>COMP70042</td>
<td>CEESI – Low Power System Design (DL)</td>
<td>C</td>
<td>A</td>
<td>D</td>
</tr>
<tr>
<td>COMP70212</td>
<td>CEESI – Self-Timed Logic (DL)</td>
<td>C</td>
<td>A</td>
<td>D</td>
</tr>
<tr>
<td>COMP60900</td>
<td>Research Project</td>
<td>C</td>
<td>A</td>
<td>D</td>
</tr>
</tbody>
</table>

**Legend for cells**

- **D** = intended learning outcomes of the programme are taught or developed by students within this course unit
- **A** = intended learning outcomes of the programme are assessed within this course unit
- **C** = compulsory course unit
- **O** = optional course unit

- **CEESI = Low Power Systems Integration**

**NB** The other course units are provided by consortium members and are governed by their institution’s quality procedures. Students will have a personal study plan negotiated with the Manchester Programme Director where guidance as to the coherence of the course units selected will be given.
7. CRITERIA FOR ADMISSION

Candidates must be able to satisfy the general admissions criteria of the University and of the School in at least one of the following ways:

Entry to the programme is by a 1st class or good 2nd class honours degree or its overseas equivalent in either Computer Science/Electronic Engineering or a joint course with at least 50% Computer Science/Electronic Engineering content. An honours degree in another subject together with sufficient relevant industrial experience is also acceptable. In exceptional circumstances, candidates without an honours degree but with considerable and relevant industrial and educational experience will be accepted.

Those applicants for whom English is not their first language must satisfy the language requirement of IELTS 7+ (or TOEFL 600, Cambridge Proficiency Grade C)

Further details are available at:
http://www.cs.man.ac.uk/Study_subweb/Postgrad/ & the CEESI website: www.ceesi.ac.uk

8. PROGRESSION AND ASSESSMENT REGULATIONS

The assessment for the MSc consists of two parts: (1) an assessment of the taught course units undertaken and (2) an assessment of the project dissertation. In order to be allowed to progress to the Research Project, students must pass the taught part of the programme. The award of an MSc is then made entirely on the basis of the project dissertation.

(1) Each student will be assessed on 90 credits' worth of coursework and examination questions. Coursework is likely to include practical laboratory exercises (individually or in groups), written essays, seminar presentations, and/or other forms appropriate to each individual course unit; for each full course unit, this usually carries two-thirds of the mark. The examinations usually consist of a two-hour paper for each course unit, carrying one third of the marks, and take place after the teaching in each semester. The regulations for passing the taught modules are as follows:

- A candidate is required to register for 90 credits' worth of course units, and will normally be considered as passing the units if:
  (i) the credit-weighted average is 50% or more on the course units, and
  (ii) the credit-weighted average of the practical work is 40% or more, and the credit-weighted average of the examination results is 40% or more, and
  (iii) course units whose results are below 40% amount to no more than 30 credits.

The final decision on whether a student passes is taken by the Low Power System Integration MSc Examination Board.

(2) The assessment of the dissertation is by two internal examiners, moderated by one external examiner. Details about dissertation format etc can be found on the University website.

All work must be original: students presenting work which is copied from any other source (unless explicitly allowed), including from other students, are breaking University regulations and will be dealt with under these regulations. Guidance on plagiarism is provided in the Programme handbook.

MSc with Distinction: An MSc with Distinction is awardable under the following circumstances:

1. The student must have passed the assessment for course units with an overall mark of at least 70% with no evidence of weakness in any subject area, and
2. the dissertation is submitted on-time and both the examiners award a mark of at least 70%. The recommendation is then passed to the External Examiner, who must agree to the recommendation for the award of a Distinction to be granted by the EPS MSc Panel.
**Postgraduate Diploma** – is awarded to a student who has been assessed on 90 credits’ worth of coursework & examination questions. These must include the two core course units (COMP70042 & COMP70212) delivered by the University of Manchester. A student will normally be considered as passing the units if:

(i) the credit weighted average is 40% or more on the course units, and

(ii) the credit weighted average of the practical work is 40% or more, and the credit weighted average of the examination results is 40% or more, and

(iii) course units whose overall results are below 40% amount to no more than 30 credits.

The Diploma can only be awarded as compensation to a student who fulfils the above criteria and has achieved a mark of 40-49% in the dissertation assessment of the Masters programme (30 credits) Where the project has been undertaken under the supervision of an academic in the University of Manchester.

**Postgraduate Certificate** – is awarded to students who have successfully taken 60 credits’ worth of coursework & examination questions with a result of 40% or more in each course unit. These must include the two core course units (COMP70042 & COMP70212) delivered by the University of Manchester.

**NB The new University will have MSc regulations (as opposed to GSSEM as at present) – These have yet to be developed**

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